

CIRCUIT DESIGN METHOD, APPARATUS, AND PROGRAM

ABSTRACT OF THE DISCLOSURE

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A circuit design method able to design a processing circuit to be small in scale when designing a processing circuit for performing a plurality of different processings on predetermined data, comprising a first
10 step of identifying second processings performing the same processing on the same data among pluralities of second processings forming each of a plurality of first processings when designing a processing circuit for applying a plurality of different first processings on
15 predetermined data and a second step of designing a processing circuit having a processing circuit shared by the plurality of first processings and for performing the second processings identified at the first step.